

DATASHEET

Module TYPE	DDR 1GB/512MB/256MB/128MB		
Module 11PE	DIMM		
Module speed	PC-3200		
CAS Latency	CL-3		
Pin	184pin		
SDRAM Operating Temp	0 ℃ ~70 ℃		

Total Solution For Industrial DRAM Module



1. Features

Key Parameter

Industry	Speed	Data Rate MT/s		tRCD	tRP	tRC	
Nomenclature	Grade	CL=2	CL=2.5	CL=3	(ns)	(ns)	(ns)
PC-3200	-	266	333	400	15	15	55

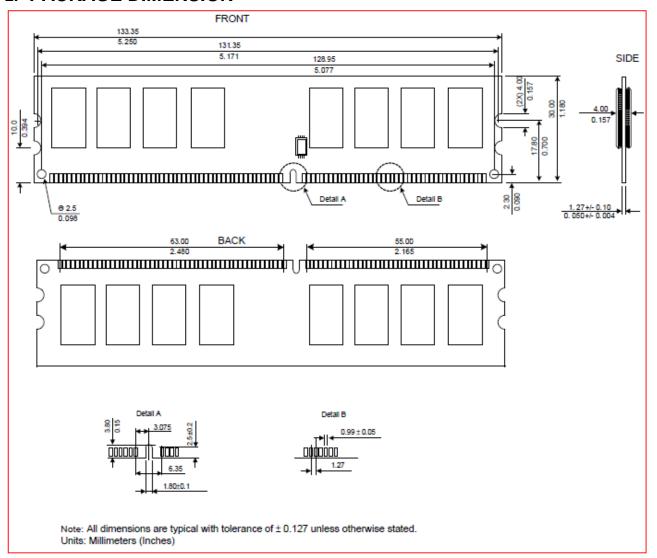
- JEDEC Standard 184-pin Dual In-Line Memory Module Automatic and controlled precharge commands.
- Intend for 400MHz applications
- Inputs and Outputs are SSTL-18 compatible
- VDD=VDDQ= 1.8 Volt \pm 0.1
- Differential clock input
- All inputs are sampled at the positive going edge of the system clock
- Bi-Directional data strobe with one clock cycle preamble and one-half clock post-amble
- Address and control signals are fully synchronous to positive clock edge.
- Auto Refresh (CBR) and Self Refresh Modes support.
- Serial Presence Detect with EEPROM

- Auto & self refresh 7.8 μ s (TA \leq +85°C)
- Golden Contactor
- SDRAM Operation Temperature (Note 1)
- $0^{\circ}C \leq TA \leq +85^{\circ}C$
- Programmable Device Operation:
- Burst Type: Sequential or Inteleave
- Operation: Burst Read and Write
- Device CAS# Latency: 6
- Burst Length: 4, 8
- RoHS Compliant (Section 15)

Note: 1. The refresh rate is required to double when Tc exceeds 85°C.



2. PACKAGE DIMENSION



Note: Device position is only for reference.